

Single Output Models



Size: 0.50 x 0.37 x 0.35 inches (12.7 x 9.3 x 8.9 mm)

Dual Output Models



Size: 0.60 x 0.37 x 0.35 inches (15.3 x 9.3 x 8.9 mm)

FEATURES

- Up to 2 Watts Output Power
- Single & Dual Outputs
- 1000VDC I/O Isolation
- High Accuracy of Pin Planarity
- MTBF > 2.000.000 Hours
- -40°C to +85°C Operating Temperature
- SMT Package with Industry Standard Pin-out
- Qualified for Lead-free Reflow Solder Processes
 According to IPC/JEDEC J-STD-020D
- Tape & Reel Packaging Available

DESCRIPTION

The DCMSAU2 series of DC/DC converters provides 2 Watts of output power in a miniature SMT package. These converters operate over input voltage ranges of 4.5~5.5VDC, 10.8~13.2VDC, and 21.6~26.4VDC. This series also has single and dual output voltages of 5V, 12V, ±5V, ±12V, and ±15VDC. The DCMSAU2 series' impressive efficiencies enable these modules to deliver their fully rated output power from -40°C to +80°C without a heatsink or forced-air cooling. The very small footprint of these converters makes them an ideal solution for many applications where a voltage has to be isolated such as for noise reduction, ground loop elimination in digital interfaces, or where a converted voltage is required. These converters are also fully qualified for the higher temperature profile used in lead-free reflow solder processes. These converters can also be supplied in tape&reel packaging for use in automated SMD production lines.

			MOL	EL SEL	ECHON	LIABLE	:			
			S	INGLE OU	TPUT MOI	DELS				
Model Number	Input Voltage	Output Voltage	Output Current		Input Current		Load	Output	F(():1:	Maximum
			Min	Max	No Load	Max Load	Regulation	Power	Efficiency	Capacitive Load
DCMSAU505-2	5 VDC (4.5 – 5.5	5 VDC	8mA	400mA	60mA	519mA	11%	2W	77%	47µF
DCMSAU512-2	(4.5 – 5.5 VDC)	12 VDC	3mA	165mA	OUITA	488mA	11%	2W	81%	10μF
DCMSAU1205-2	12 VDC (10.8 – 13.2 VDC)	5 VDC	8mA	400mA	30mA	213mA	8%	2W	78%	47µF
DCMSAU1212-2		12 VDC	3mA	165mA		201mA	6%	2W	82%	10μF
DCMSAU2405-2	24 VDC	5 VDC	8mA	400mA	45	106mA	8%	2W	78%	47µF
DCMSAU2412-2	(21.6 – 26.4 VDC)	12 VDC	3mA	165mA	15mA	101mA	5%	2W	81%	10µF
				DUAL OUT	PUT MOD	ELS				
Model Number	Input Voltage	Output Voltage	Output Current		Input Current		Load	Output		Maximum
			Min	Max	No Load	Max Load	Regulation	Power	Efficiency	Capacitive Load
DCMSAU505D-2	5 VDC	±5 VDC	±4mA	±200mA	60mA	519mA	12%	2W	77%	±10μF
DCMSAU512D-2	(4.5 – 5.5	±12 VDC	±1.5mA	±83mA		504mA	7%	2W	79%	±4.7μF
DCMSAU515D-2	VDC)	±15 VDC	±1mA	±66mA		501mA	7%	2W	79%	±4.7μF
DCMSAU1212D-2	12 VDC	±12 VDC	±1.5mA	±83mA	30mA	202mA	5%	2W	82%	±4.7μF
DCMSAU1215D-2	(10.8 – 13.2 VDC)	±15 VDC	±1mA	±66mA		201mA	5%	2W	82%	±4.7μF
DCMSAU2412D-2	24 VDC	±12 VDC	±1.5mA	±83mA	15mA	102mA	5%	2W	81%	±4.7μF
DCMSAU2415D-2	(21.6 – 26.4 VDC)	±15 VDC	±1mA	±66mA		100mA	5%	2W	82%	±4.7μF
NOTES										

MODEL SELECTION TABLE

NOTES

- 1. The DCMSAU2 series requires a minimum output loading to maintain specified regulations. Operation under no-load conditions will not damage these devices; however they may not meet all listed specifications.
- 2. All DC/DC converters should be externally fused at the front end for protection.
- 3. Other input and output voltages may be available, please contact factory.
- *Due to advances in technology, specifications are subject to change without notice.



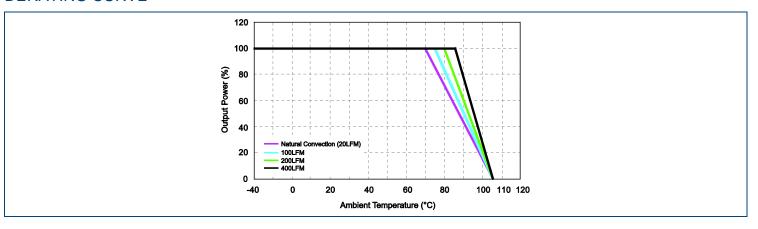
SPECIFICATIONS: DCMSAU2 SERIES

All specifications are based on 25°C, Nominal Input Voltage, and Maximum Output Current unless otherwise noted. We reserve the right to change specifications based on technological advances.

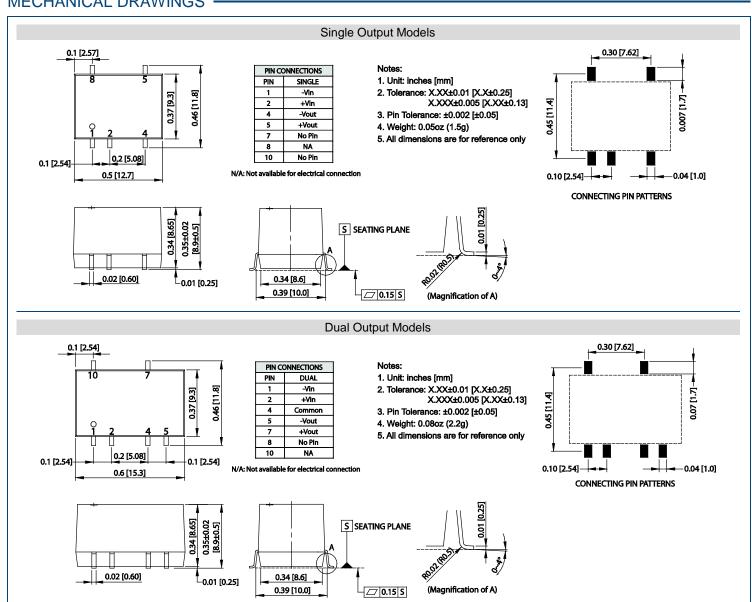
SPECIFICATION	TEST CONDITIONS	Min	Тур	Max	Unit					
INPUT SPECIFICATIONS										
	5VDC nominal input models	4.5	5	5.5						
Input Voltage Range	12VDC nominal input models	10.8	12	13.2	VDC					
, and a sign of grant gr	24 VDC nominal input models	21.6	24	26.4						
	5VDC nominal input models	-0.7		9						
Input Surge Voltage (1sec, max.)	12VDC nominal input models	-0.7		18	VDC					
, , , , , , , , , , , , , , , , , , ,	24 VDC nominal input models	-0.7		30						
Reverse Polarity Input Current				0.3	Α					
Input Current			See	Table	I					
Internal Power Dissipation				650	mW					
Input Filter		Internal capacitor								
	5VDC nominal input models	1000mA slow-blow type								
Input Fuse	12VDC nominal input models	500mA slow-blow type								
	24 VDC nominal input models	200mA slow-blow type								
OUTPUT SPECIFICATIONS										
Output Voltage			See	Table						
Output Voltage Accuracy			±1.5	±4.0	%					
Output Voltage Balance (Dual Outputs)	Balanced loads		±0.1	±1.0	%					
Line Regulation	For Vin change of 1%		±1.2	±1.5	%					
Load Regulation	20% load to 100% load			Table						
Output Power				2	W					
Output Current			See	Table						
Minimum Load	See Note 1			Table						
Maximum Capacitive Load	1 111				See Table					
Waximum Capacitive Load			100	120	mVp-p					
Ripple & Noise (20MHz)	Over line, over load, and over temperature		100	200	mVp-p					
Trippie a rioise (2011/12)	Over line, over load, and over temperature			15	mV rms					
Temperature Coefficient			±0.01	±0.02	%/°C					
PROTECTION			±0.01	±0.02	70/ 0					
Short Circuit Protection				0.5	s					
GENERAL SPECIFICATIONS				0.5	3					
	Naminal input valtage and full load		Coo.	Table						
Efficiency Switching Frequency	Nominal input voltage and full load	50	100	120	KHz					
Isolation Voltage (I/P to O/P)	60 seconds	1000	100	120	VDC					
Isolation Resistance (I/P to O/P)	500VDC	1000			ΜΩ					
Isolation Capacitance (I/P to O/P)	100KHz, 1V		60	100	pF					
ENVIRONMENTAL SPECIFICATION										
Operating Ambient Temperature	See derating curve	-40		+85	°C					
Case Temperature	our advanting out to			+90	°C					
Storage Temperature		-50		+125	°C					
Relative Humidity	Non-condensing	30		95	% RH					
Cooling	Non-condensing		Froc sir -	convection	/0 KI					
	4.5		Free all C		00					
Lead Temperature	1.5mm from case for 10 sec.			300	°C					
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020D	Level 3 2,000,000 hours								
MTBF	MIL-HDBK-217F at 25°C, ground benign		2,000,0	00 hours						
PHYSICAL SPECIFICATIONS				// = ·						
Weight	Single Output Models	0.05oz (1.5g)								
	Dual Output Models			z (2.2g)						
	Single Output Models	0.50 x 0.37 x 0.34 inch (12.7 x 9.3 x 8.65 mm)								
Dimensions (L x W x H)		0.60 x 0.37 x 0.34 inch								
	Dual Output Models 0.60 x 0.37 x 0.34 mg (15.3 x 9.3 x 8.65 mg									
Case Material	Flammability to UL 94V-0 rated			ding	,					
		L		9						



DERATING CURVE



MECHANICAL DRAWINGS

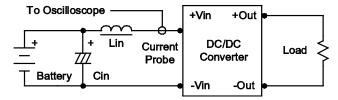




DESIGN CONSIDERATIONS

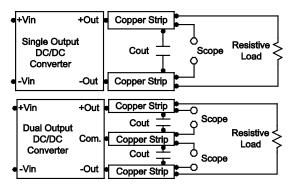
Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin (4.7 μ H) and Cin (220 μ F, ESR < 1.0 Ω at 100 KHz) to simulate source impedance. Capacitor Cin offsets possible battery impedance. Current ripple is measured at the input terminals of the module. Measurement bandwidth is 0-500 KHz.



Peak-to-Peak Output Noise Measurement Test

Use a 0.33µF ceramic capacitor. Scope measurement should be made by using a BNC socket, measurement bandwidth is 0-20MHz. Position the load between 50mm and 75mm from the DC/DC converter.



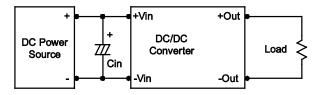
TEST SETUP

Maximum Capacitive Load

The DCMSAU2 series has a limitation of maximum connected capacitance on the output. The power module may be operated in current limiting mode during start-up, affecting the ramp-up and the start-up time. The maximum capacitance can be found in the Model Selection Table.

Input Source Impedance

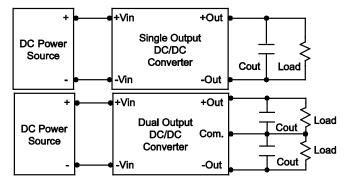
The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor at the input to ensure startup. Capacitor mounted close to the power module helps ensure stability of the unit, it is recommended to use a good quality low Equivalent Series Resistance (ESR < 1.0Ω at 100 KHz) capacitor of 2.2μ F for 5VDC input models, 1.0μ F for 12VDC input models, and 0.47μ F for 24VDC input models.





Output Ripple Reduction

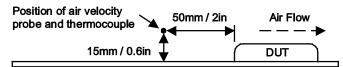
A good quality low ESR capacitor placed as close as possible across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use 1.5µF capacitors at the output.



TEST SETUP -

Thermal Considerations

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module, and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 90°C. The derating curves are determined from measurements obtained in a test setup.



COMPANY INFORMATION

Wall Industries, Inc. has created custom and modified units for over 50 years. Our in-house research and development engineers will provide a solution that exceeds your performance requirements on-time and on budget. Our ISO9001-2008 certification is just one example of our commitment to producing a high quality, well-documented product for our customers.

Our past projects demonstrate our commitment to you, our customer. Wall Industries, Inc. has a reputation for working closely with its customers to ensure each solution meets or exceeds form, fit and function requirements. We will continue to provide ongoing support for your project above and beyond the design and production phases. Give us a call today to discuss your future projects.

Contact Wall Industries for further information:

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