



## **FEATURES**

Rev C

- Industrial SMD Package
- I/O Isolation 1000VDC
- Water Washable Process Available
- Tape & Reel Package Available
- RoHS & REACH Available
- Up to 1 Watt Output Power

#### DESCRIPTION

- Single or Dual Outputs AvailableHigh Efficiency
- Short Circuit Protection
- Qualified for Lead-Free Reflow Solder Process According to IPC/JEDEC J-STD-020D.1

The PF series of DC DC converters offers up to 1 watt of output power in a compact SMD package. This series has I/O isolation of 1000VDC and offers single or dual outputs. The PF series is RoHS compliant, has high efficiency, and short circuit protection. This series has water washable process and tape & reel packages available, and it is qualified for lead-free reflow solder process according to IPC/JEDEC J-STD-020D.1

MODEL SELECTION TABLE										
Single Output										
Model Number	Input Voltage Range	Output Voltage	Output Current		Ripple &	Input Current		Output	Load Regulation	Efficiency
			Min Load	Max Load	Noise	No Load	Full Load	Power	Load Regulation	Enciency
PF5S33-300	5VDC (4.5~5.5VDC)	3.3VDC	6mA	300mA	120mVp-p	30mA	264mA	1W	10%	75%
PF5S5-200		5VDC	4mA	200mA			250mA		10%	80%
PF5S9-110		9VDC	2mA	110mA			254mA		10%	78%
PF5S12-84		12VDC	1.5mA	84mA			252mA		8%	80%
PF5S15-67		15VDC	1mA	67mA			248mA		7%	81%
PF12S33-300	12VDC (10.8~13.2VDC)	3.3VDC	6mA	300mA	120mVp-p	15mA	110mA	1W	8%	75%
PF12S5-200		5VDC	4mA	200mA			103mA		8%	81%
PF12S9-110		9VDC	2mA	110mA			106mA		8%	78%
PF12S12-84		12VDC	1.5mA	84mA			104mA		5%	81%
PF12S15-67		15VDC	1mA	67mA			102mA		5%	82%
PF24S33-300	24VDC (21.6~26.4VDC)	3.3VDC	6mA	300mA	120mVp-p	8mA	57mA	1W	8%	73%
PF24S5-200		5VDC	4mA	200mA			53mA		8%	79%
PF24S9-110		9VDC	2mA	110mA			54mA		8%	77%
PF24S12-84		12VDC	1.5mA	84mA			53mA		5%	80%
PF24S15-67		15VDC	1mA	67mA			52mA		5%	80%

MODEL SELECTION TABLE										
Dual Output										
Model Number	Input Voltage	Output	Output Current		Ripple &	Input Current		Output	Load Regulation	Efficiency
	Range	Voltage	Min Load	Max Load	Noise	No Load	Full Load	Power	Load Regulation	Enciency
PF5D5-100	5VDC (4.5~5.5VDC)	±5VDC	±2mA	±100mA	120mVp-p	30mA	267mA	1W	10%	75%
PF5D9-55		±9VDC	±1mA	±55mA			260mA		10%	76%
PF5D12-42		±12VDC	±0.8mA	±42mA			255mA		8%	79%
PF5D15-33		±15VDC	±0.7mA	±33mA			251mA		7%	79%
PF12D5-100	12VDC (10.8~13.2VDC)	±5VDC	±2mA	±100mA	- 120mVp-p	15mA	111mA	1W	8%	75%
PF12D9-55		±9VDC	±1mA	±55mA			109mA		8%	76%
PF12D12-42		±12VDC	±0.8mA	±42mA			105mA		5%	80%
PF12D15-33		±15VDC	±0.7mA	±33mA			103mA		5%	80%
PF24D5-100	24VDC (21.6~26.4VDC)	±5VDC	±2mA	±100mA	120mVp-p	9mA	56mA	1W	8%	74%
PF24D9-55		±9VDC	±1mA	±55mA			55mA		8%	75%
PF24D12-42		±12VDC	±0.8mA	±42mA			53mA		5%	79%
PF24D15-33		±15VDC	±0.7mA	±33mA			52mA		5%	79%



SPECIFICATIONS

PECIFICATION IPUT SPECIFICATIONS		EST CONDITIONS	Min	Тур	Max	Unit		
	5V input models		4.5	5	5.5			
put Voltage Range	12V input models	10.8	12	13.2				
	24V input models		21.6	24	26.4			
	5V input models		-0.7		9			
Input Surge Voltage (1 sec. max.)	12V input models		-0.7		18			
but burge voltage (1 see. max.)	24V input models		-0.7		30			
put Filter			-0.7	Internal (	Capacitor			
everse Polarity Input Current	Single Output Models				0.3	A		
ternal Power Dissipation	Single Output Models				450	mW		
UTPUT SPECIFICATIONS					430	11100		
utput Voltage				See	Table			
oltage Accuracy				±1.0	±3.0	%/Vnc		
ne Regulation	For Vin Change of 1%			±1.0	±3.0 ±1.5	%		
bad Regulation	lo=20% to 100%				-			
		Laada	See Model Selection Guide ±0.1 ±1.0 %					
utput Voltage Balance utput Power	Dual Outputs, Balanced	Loaus		-	Table	-70		
utput Current				2	Table	-		
aximum Capacitive Load				33		μF		
ipple & Noise	0-20MHz Bandwidth				120	mVp-		
emperature Coefficient				±0.01	±0.02	%/°C		
ROTECTION			1	1		1		
nort Circuit Protection	Automatic Recovery				0.5	Sec		
NVIRONMENTAL SPECIFICATIO								
perating Ambient Temperature	Natural Convection		-40		+85	°C		
torage Temperature			-50		+125	°C		
ase Temperature					+90	°C		
umidity	Non-Condensing				95	% R		
ooling				Natural C	onvection			
ead-free Reflow Solder Process			I	PC/JEDEC	JSTD-020D	.1		
TBF (calculated)	MIL-HDBK-217F@25°C	, Ground Benign		2,000,000		hour		
ENERAL SPECIFICATIONS								
fficiency				See	Table			
witching Frequency			50	100	140	KHz		
	60 Seconds		1000					
olation Voltage	1 Seconds		1200			VDC		
olation Resistance	500VDC		1000			MΩ		
olation Capacitance	100KHz, 1V			40	100	pF		
oisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020E	) 1		-	vel 3	<u> </u>		
HYSICAL SPECIFICATIONS								
		5V & 12V models		0.0530	z (1.5g)			
	Single Output	24V models	0.063oz (1.8					
/eight		5V & 12V models	0.063oz (1.8g)					
	Dual Output	24V models	0.078oz (2.2g)					
		24V models						
		5V & 12V models	1.	0.50in x 0.31in x 0.27in (12.7mm x8.0mm x 6.8mm)				
	Single Output		0.50in x 0.33in x 0.31in					
		24V models	(12.7mm x 8.3mm x 7.8mm)					
imensions (L x W x H)			0.60in x 0.31in 0.27in					
· · · · ·	Dual Output	5V & 12V models						
			(15.24mm x 8.0mm x 6.8mm) 0.60in x 0.33in x 0.31in					
		24V models	(4)					
				5.24mm x 8.				
ase Material			P	lastic UL94		ng		
ammability				UL9	4V-0			
AFETY & EMC CHARACTERISTI	CS							
ompliance				RoHS,	REACH			
		NOTES						
(1) These power convertors	line a minimum output loading to a	naintain specified regulation, operation und	er no-load conditions	will not dome	ae those med	ules:		
<ol> <li>These power converters required however they may not meet a</li> </ol>		naman specified regulation, operation und	er no-ioau conullions	will not dama	ye mese moa	ules,		
	converter by a slow blow fuse in t	he input supply line.						

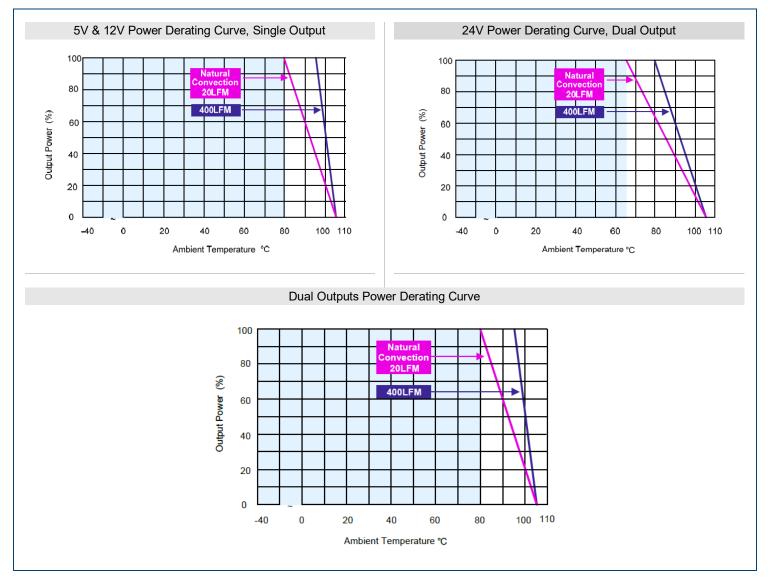
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(5) Specifications are subject to change without notice.
 \*Due to advances in technology, specifications subject to change without notice.

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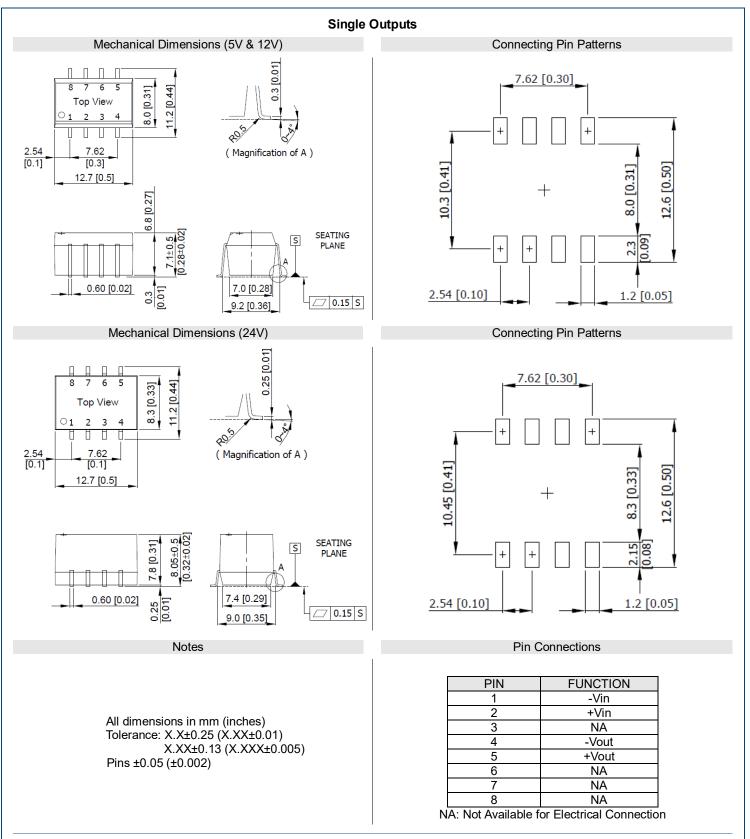
# DERATING CURVES



Rev C

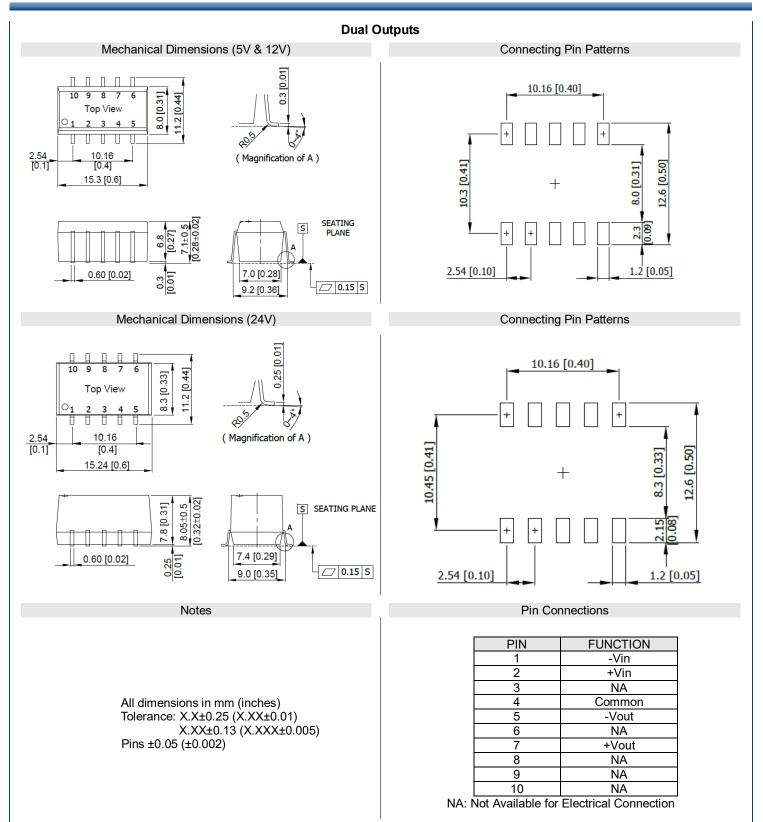


MECHANICAL DRAWINGS



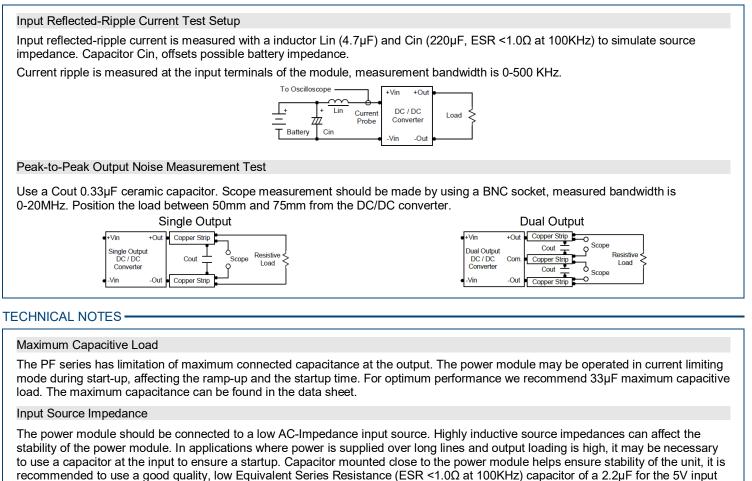
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#### TEST SETUP-



devices, a 1.0 $\mu$ F for the 12V input devices and a 0.47 $\mu$ F for the 24V input devices.



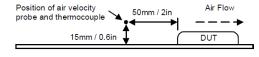
# **Output Ripple Reduction**

A good quality low ESR capacitor placed as close as practicable across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use 0.47µF capacitors at the output.



#### Thermal Considerations

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 90°C. The derating curves are determined from measurements obtained in a test setup.







## COMPANY INFORMATION

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