

TECHNICAL DATASHEET Rev. D

MPQ24S5-50C

50 W DC-DC Converter 10-36 Vdc Input 5 Vdc Output at 10 A Quarter-Brick Package



Features:

- Over 87% Efficient at Full Load
- Fast Transient Response
- Operation to No Load
- Output Trim ±10%
- Remote ON/OFF
- Remote Sense Compensation
- Low Output Ripple



- Fixed Switching Frequency
- Output Over Current Protection
- Output Short Circuit Protection
- Over Temperature Protection
- 1000 V Isolation
- 100% Burn In
- Heatsink Available

Description:

The MPQ24 series is a high density, low voltage input quarter brick converter that incorporates the desired features required in today's demanding applications while maintaining low cost. When performance, reliability, and low cost are needed, the MPQ series delivers.

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Technical Specifications Model No. **MPQ24S5-50C** All specifications are based on 25°C, Nominal Input Voltage and Maximum Output Current unless otherwise noted. We reserve the right to change specifications based on technological advances. **Related condition** Min Nom Max Unit **SPECIFICATION** Switching Frequency 200 kHz --INPUT (V_{in}) 10 Vdc Operating Voltage Range 24 36 UVLO Turn On at 10.0 9.8 10.3Vdc UVLO Turn Off at 9.4 9.7 9.9 Vdc UVLO Hysterisis 0.3 Vdc _ -Maximum Input Current (Graph 3) Low Line 5.8 А -No Load Input Current (Graph 5) No Load 0.081 А -Input Current under "Remote Off" (Graph 6) Active High Unit 3.1 mA Reflected Ripple Current (Photos 1 & 2) Measured with a 120 µF Alum. Elect. Input Capacitor 192 mΑ _ Input Surge Voltage 100 mS 50 Vdc 87 % _ **EFFICIENCY** (Graph 1) OUTPUT (V₀) 5.030 4.970 Vdc 5.000 Voltage Set Point ±Sense shorted to ±Vout -0.60 +0.60% 4.505.50 Voltage Adjustment (Table 2) Max Output limited to 90W 5.00 Vdc -10% +10%Load Regulation (Graph 7) +Sense shorted to +Vout 0.02 0.1 % ±Sense shorted to ±Vout 0.02 Line Regulation (Graph 8) 0.1 % % / °C Temperature Drift (Graph 9) 0.013 0.025 _ Vdc Max Output limited to 50W 5.50 Remote Sense Compensation _ (as measured at the converter output pins) 10% % Ripple (Photos 7, 9 & 11) With 1 µF ceramic & 10 µF Tantalum 82 100 mV_{pk-pk} 82 150 Spikes (Photos 7, 9 & 11) With 1 µF ceramic & 10 µF Tantalum mV_{pk-pk} 0 Current 10.0 А Power Limited-Dependent upon SENSE compensation Current Limit 12 14 16 А and TRIM adjustment Over Voltage Limit 5.8 54.0 58 Vdc DYNAMIC RESPONSE Load step $/\Delta V$ (Photos 8, 10 & 12) 25% to 100% Io, di/dt=0.025A/uS 264 mV Recovery Time (Photos 8, 10 & 12) Recovery to within 1% Nominal Vout 1 ms From Vin (min) to Vout (mom) Turn On Delay (Photo 3) 10 ms _ _ Turn On Overshoot (Photos 3 & 5) Full Load Resistive 0.0 _ % Hold Up Time (Photo 4) 0 From Vin (min) to VULVO Turn Off mS **REMOTE ON/OFF** Active High or Active Low (For Active Low add an 'R' to the end of the Part Number) Remote ON - Active High Min High to Enable 1.5 Vdc 0.3 Remote OFF - Active High Max Low to Disable Vdc Remote ON/OFF pin Floating - Active High Over Operating Voltage Range 5.2 Vdc 1.6 V_{ON/OFF}=0V, Vin=36 V 0.15 ION/OFF Sink to pull low - Active High mА -Remote ON - Active Low Max Low to Enable 0.8Vdc Vdc Remote OFF - Active Low Min High to Disable 2.1 -Remote ON/OFF pin Floating - Active Low 6.2 Over Operating Voltage Range 2.3 ION/OFF Sink to pull low - Active Low V_{ON/OFF}=0V, Vin=36V 0.6 mA 0 ION/OFF Source to drive high - Active High or Low mA --Turn On Delay – (Photo 5) Enable (max Low) to Vout (min) 3 ms Turn Off Delay – (Photo 6) Enable (0V) to Vout (min) 100 μS ISOLATION Input-Output 1 minute 1000 Vdc --Input/Output-Chassis 1 minute 1000 Vdc Isolation Resistance at 25°C 20 GΩ 0.01 Isolation Capacitance μF THERMAL Max. Ambient limited by Derating Curves (Graph 2) -40 25 Graph 2 °C Ambient (Graph 2) °C Over Temperature Protection Case Temperature 110 -55 125 °C Storage Temperature 1,250,300 MTBF Calculated Using Bellcore TR-332 Method 1 case 3 hours MECHANICAL See Figure 1 Weight 61 σ

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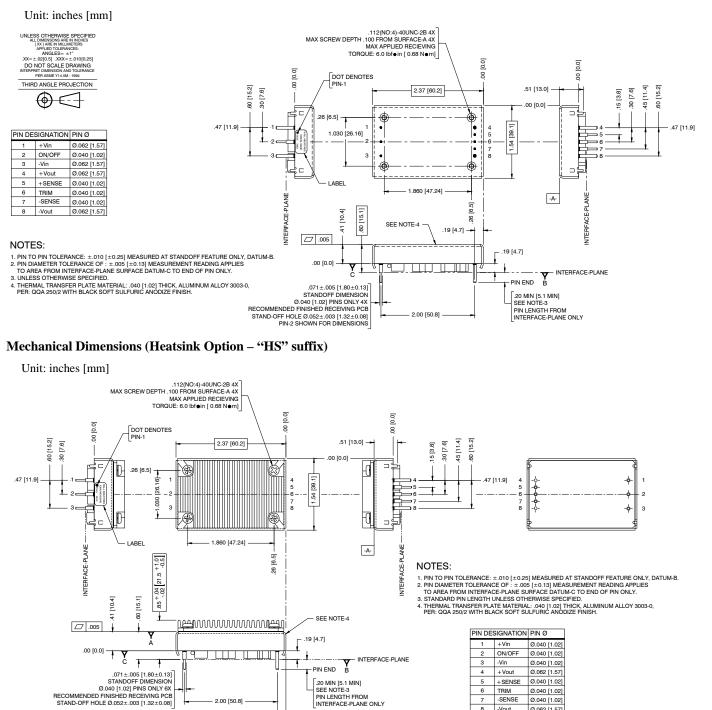
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Table 1: Pin Assignments

Pin #	Pin Name	Function	Comments
1	+Vin	Positive Input	
2	Enable	Remote On/Off	If not used, leave open for standard unit, short to -Vin on 'R' units.
3	-Vin	Negative Input	
4	+Vout	Negative Output	
5	+SENSE	Negative Remote Sense	If not used, short to –Vo.
6	TRIM	Output Voltage Trim	If not used, leave open.
7	-SENSE	Positive Remote Sense	If not used, short to +Vo.
8	-Vout	Positive Output	

Figure 1: Mechanical Dimensions



.19 [4.7] ---

8 -Vout Ø.062 [1.57]

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DESIGN CONSIDERATIONS

Under Voltage Lock Out (UVLO)

The converter output is disabled until the input voltage exceeds the UVLO turn-on limit. The converter will remain ON until the input voltage falls below the UVLO turn-off limit.

Over Current Protection

The converter is protected from short circuit and over current conditions. Upon sensing an over current, the output will begin to drop (or 'foldback') limiting the output power. Further increasing the output current will cause the converter to shut off and then restart (or 'hiccup') until the overcurrent condition is removed. Shorting the output will cause the converter to immediately enter the 'hiccup' mode.

Over Temperature Protection

The converter is protected from over temperature conditions. Upon exceeding this temperature, the converter will shut down. The converter will automatically recover once the over tempature condition is removed.

Input Filter

No additional input capacitor is needed for the power supply to operate. However, due to the low voltage, high input current nature of the power supply, it is highly recommended that a minimum 100 uF/50 V electrolytic type input bulk capacitor be added to reduce input ripple voltage and current. Refer to Photos 1 and 2 for an example. For an even further reduction of input ripple, an inductor may be placed between the source and the previously mentioned capacitor. Additionally, a 1-10 uF ceramic capacitor may be added in front of the inductor to form pi-filter. No inductor should be placed between the capacitor and the input to the converter.

Output Filter

No additional output capacitor is needed for the power supply to operate. However, to reduce the ripple and noise on the output, additional capacitance may be added. Usually, a ceramic capacitor between 1 and 100 uF works best for reducing ripple and spike noise. Also, capacitance in the form of a low-esr, surge robust tantalum capacitor (ie: Kemet T495 Series) may also be placed across the output in order reduce ripple, and improve the transient peak-to-peak voltage deviation (see Photos 7 to 11). Due to the low-esr nature of the output of the power supply, adding typical aluminum electrolytic capacitors to the output will not help much in reducing ripple or transient deviations, unless the load is some distance from the power supply output. Then, these capacitors should be placed at the load.

Remote Sense

To improve regulation at the load, route the connections from the -Sense and the +Sense pins to the –Vout and +Vout connections at the load. This will force the converter to regulate the voltage at the load and not at the pins of the converter. If it is not desired to use the Remotes Sense feature, the –Sense and +Sense pins should be shorted to the -Vout and +Vout pins respectively. However, no damage to the converter will occur if the Sense pins are left open.

Fusing

It is required that the input to the converter be supplied with a maximum 10 A, 250 V rated fuse UL Listed or R/C fuse.

Safety

The MPQ24 series is designed to meet EN60950 Safety of Information Technology Equipment. The isolation provided by the MPQ24 series is a Basic insulation in accordance with EN60950. SELV output reliability is maintained only if the input to the converter is a SELV source. To maintain SELV reliability, if either +Vin or -Vin is connected to chassis, either +Vout or -Vout must also be connected to chassis. Otherwise, both the input and the output must not be connected to chassis.

PCB Layout Considerations

Due to the Basic isolation provided by the converter, caution must be observed in routing traces more than 2 mm inward of any input or output pins on the top layer of the pcb board underneath the converter. Also, due to noise coupling and isolation requirements, no power or ground planes or any signal traces should be routed on the top layer of the pcb underneath the converter. Due to comon noise coupling, input or output power and ground planes should not be poured across the input to output on any layers underneath the converter. Instead, it is best to provide separate input and output power and ground traces on the bottom or an inner layer with a miminum of 1 mm separation between traces on the same layer.

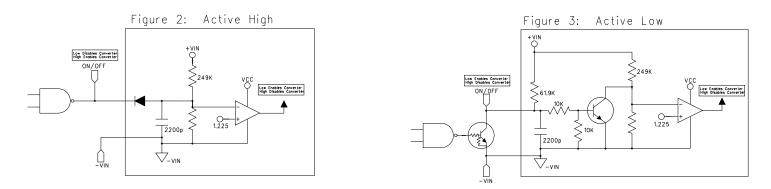
Lastly, as the case/heatsink is floating metal, caution must also be observed to provide appropriate spacing (minimum 1.4 mm for Pollution degree 2 Material Group IIIa + IIIb) around the case/heatsink or risk reducing the input to output spacing and violating Basic insulation requirements.

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Remote ON/OFF

This converter has the ability to be remotely turned ON or OFF. The series may be ordered Active-High or Active-Low (place an option 'R' at the end of the part number). Active-High means that a logic high at the ENABLE pin will turn ON the supply (Figure 2). With Active-High, if the ENABLE pin is left floating, the supply will be enabled. Active-Low means that a logic low at the ENABLE pin will turn ON the supply (Figure 3). With Active-Low, if the ENABLE pin is left floating, the supply will be disabled. If remote On/Off is not used on an Active-Low supply, short the Enable pin to –Vin.

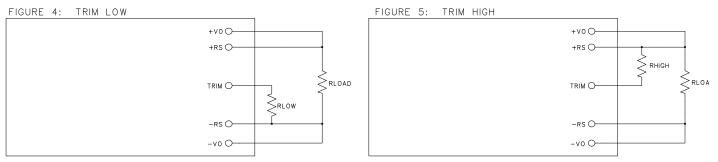


Output Voltage Trim

The output is adjustable from +/-10% of the output voltage. To adjust the output voltage low, place a resistor between the TRIM and -SENSE pins (Figure 4). To adjust the output voltage high, place a resistor between the +SENSE and TRIM pins (Figure 5). The value of the TRIM resistor with respect to the desired output voltage can be found in Table 2 or derived from the following equations:

$$R_{Trim - Low} = \frac{511}{\Delta\%} - 5.11 \quad (\text{in } \text{k}\Omega) \qquad R_{Trim - High} = \frac{5.11 \cdot V_{onom} \cdot (\Delta\% + 100)}{2.5 \cdot \Delta\%} - \frac{511}{\Delta\%} - 5.11 \quad (\text{in } \text{k}\Omega)$$

where $\Delta\% = \text{Percent Trim} = \left| \frac{V_o^{+/-} - V_{onom}}{V_o^{+/-}} \right| \cdot 100$



NOTE 1: CONNECT TRIM RESISTOR AS CLOSE TO CONVERTER PINS AS POSSIBLE

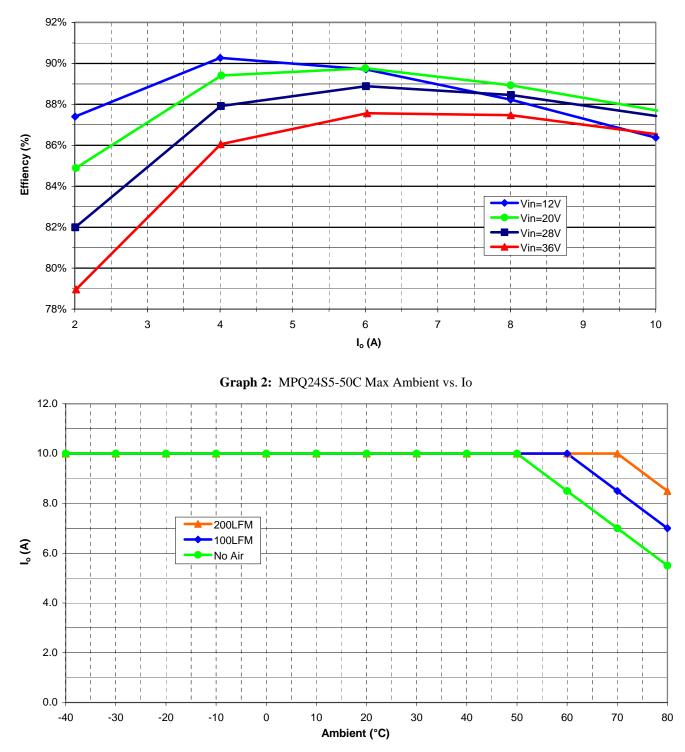
Table 2: Trim Resistor Values (in $k\Omega$)

Percent	TRIM Low		TRIM High	
Trim	Vout	R _{Low}	Vout	R _{High}
1%	4.950	500.78	5.050	511.00
2%	4.900	245.28	5.100	255.50
3%	4.850	160.11	5.150	170.33
4%	4.800	117.53	5.200	127.75
5%	4.750	91.98	5.250	102.20
6%	4.700	74.95	5.300	85.17
7%	4.650	62.78	5.350	73.00
8%	4.600	53.66	5.400	63.88
9%	4.550	46.56	5.450	56.78
10%	4.500	40.88	5.500	51.10

Note 2: While decreasing the output voltage, the maximum output current remains the same, and while increasing the output voltage, the output current is reduced to maintain the total output power at 50 W.

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TECHNICAL DATASHEET MPQ2485-50C

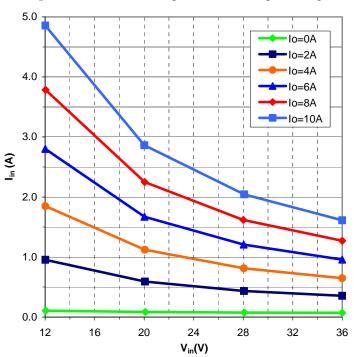


Graph 1: MPQ24S5-50C Efficiency vs. Output Current

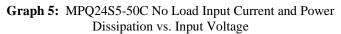
Note 3: When trimming output high, I_o vs. Ambient is derated by power. ie: from Graph 2, find the maximum current at the desired ambient and airflow, and multiply this current by the nominal voltage to get the maximum power. Divide this power by the desired trimmed high voltage to get the maximum current at that ambient. When trimming low, the maximum current stays the same as shown in Graph 2.

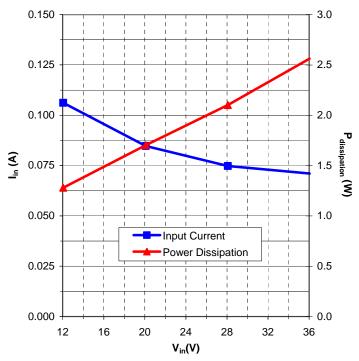
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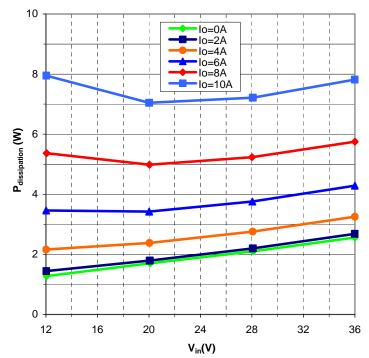


Graph 3: MPQ24S5-50C Input Current vs. Input Voltage



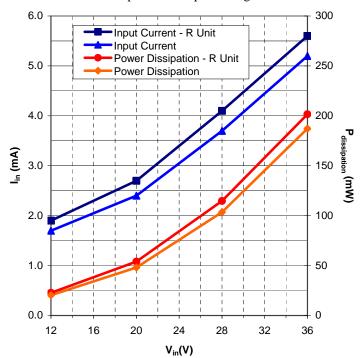


Note 4: Voltage measurements taken where the output pins are soldered into test board.



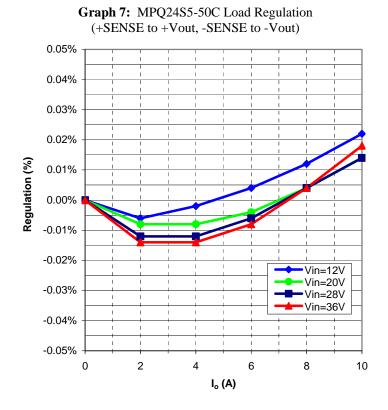
Graph 4: MPQ24S5-50C Power Dissipation vs. Input Voltage

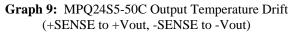
Graph 6: MPQ24S5-50C "Remote Off" Input Current and Power Dissipation vs. Input Voltage

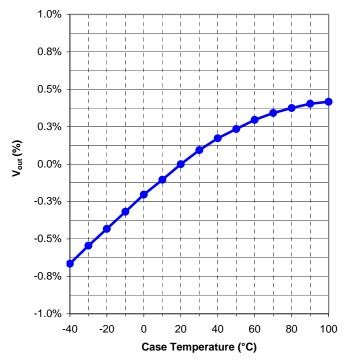


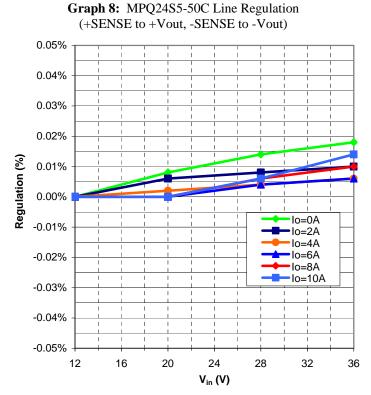
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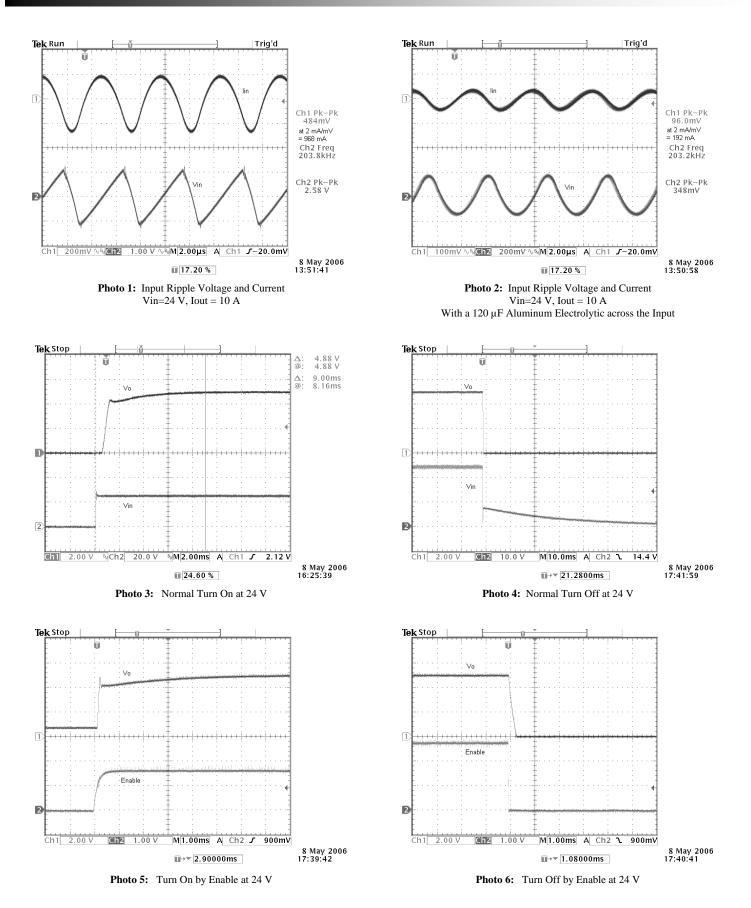




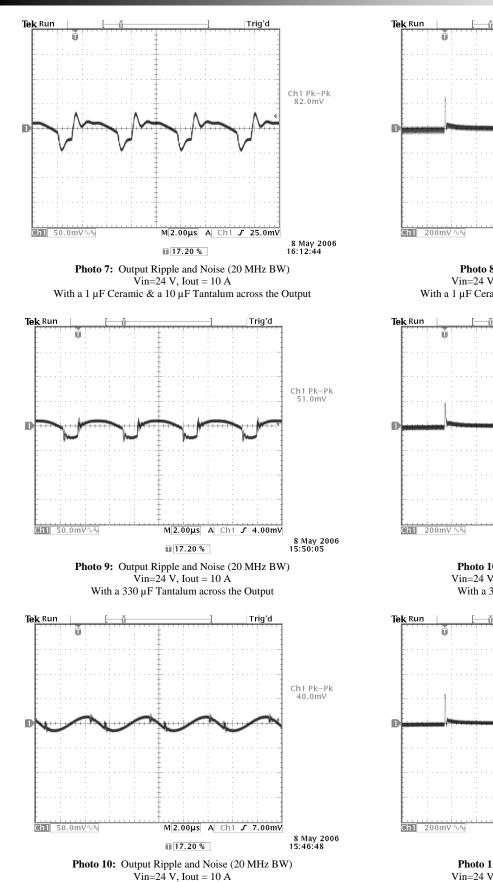
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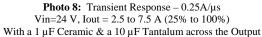
 Ch1 Max
 264mV

 Ch1 Min
 264mV

 Ch1 Min
 276mV

 Ch1 Min
 276mV

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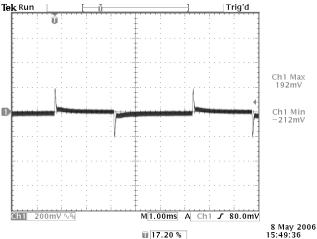


Photo 10: Transient Response $-0.25A/\mu s$ Vin=24 V, Iout = 2.5 to 7.5 A (25% to 100%) With a 330 μ F Tantalum across the Output

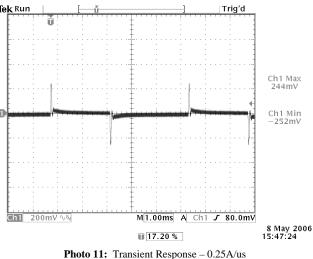


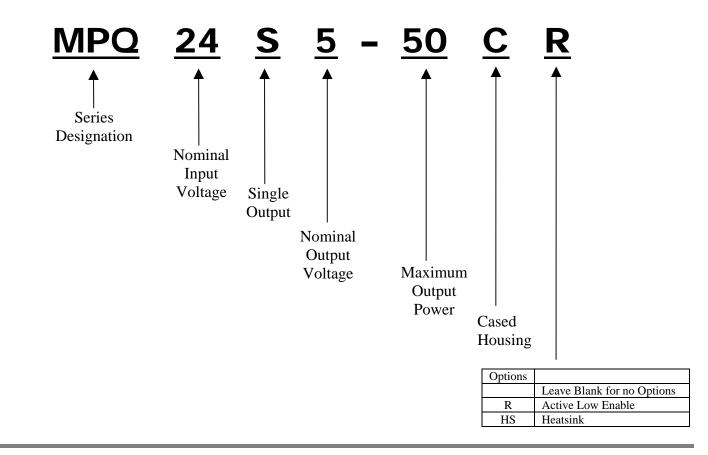
Photo 11: Transient Response – 0.25A/us Vin=24 V, Iout = 2.5 to 7.5 A (25% to 100%) With a 100 μ F Ceramic across the Output

With a 100 µF Ceramic across the Output

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Company Information:

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